

Notice of References Cited

Application/Control No.

09/775,835

Applicant(s)/Patent Under
Reexamination
YOO, WOO SIK

Examiner

Lisa A Kilday

Art Unit

2829

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, "Silicon Processing for the VLSI Era", 1990, Volume 1-Process Technology, pp. 164-5, 169-178, 182-4, 194, Volume 2-Process Integration: pp. 331, 431, 434-5.
	V	Microchip Fabrication, Peter Van Zant, 4 th Edition, 2000, pp. 363-393
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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